

FIG. 1

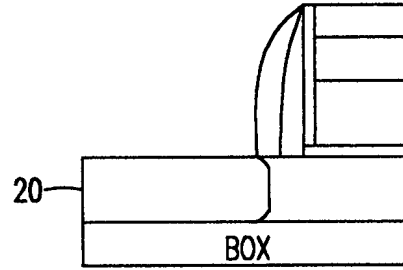
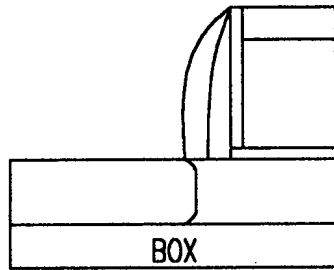


FIG. 2

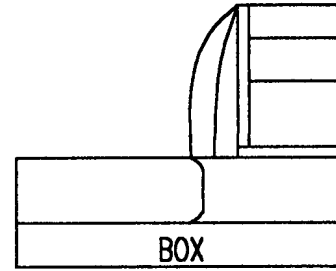
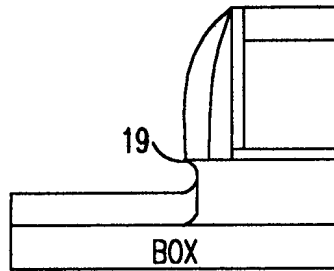


FIG. 3

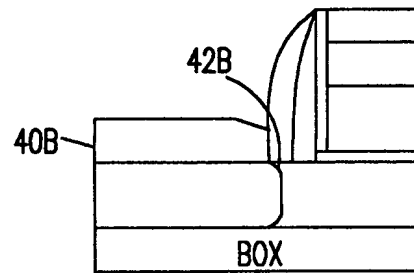
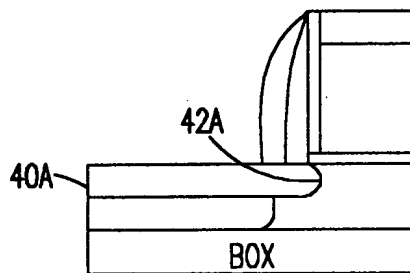


FIG. 4

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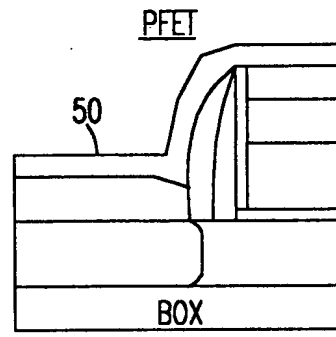
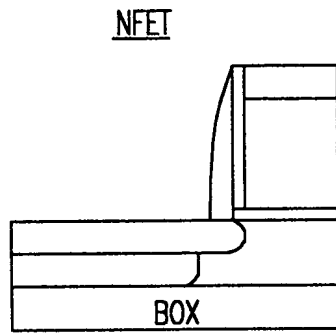


FIG. 5

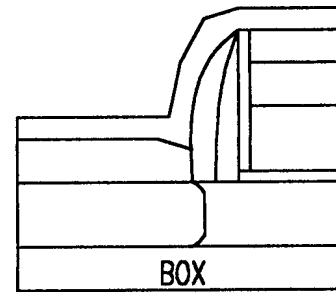
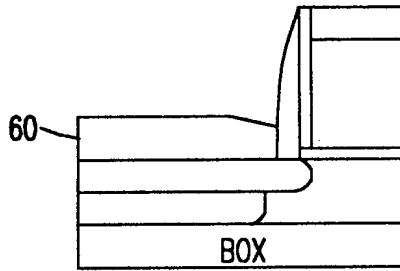


FIG. 6

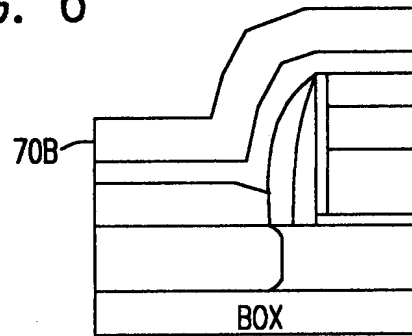
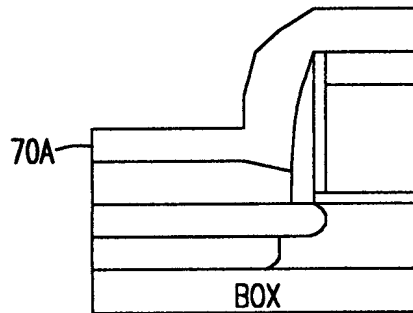


FIG. 7

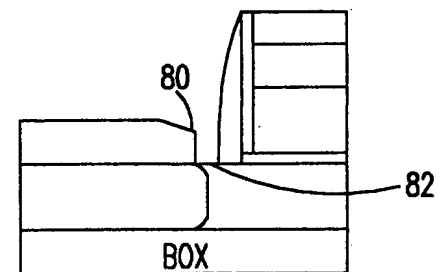
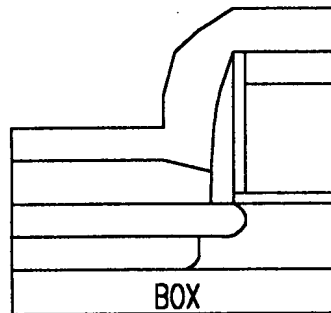


FIG. 8

0994372660

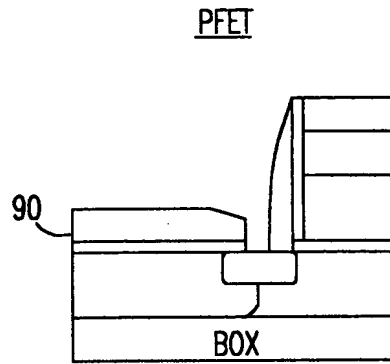
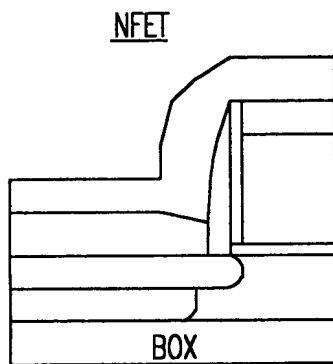


FIG. 9

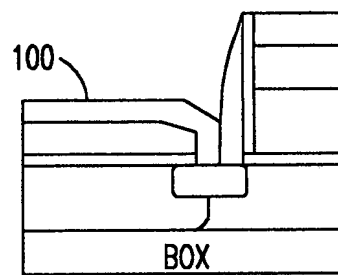
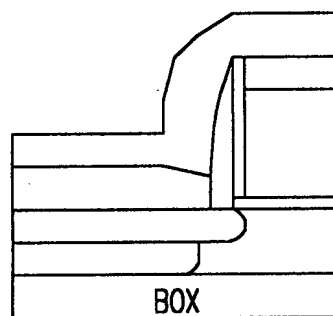


FIG. 10

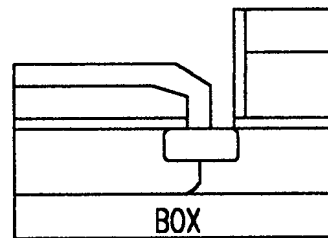
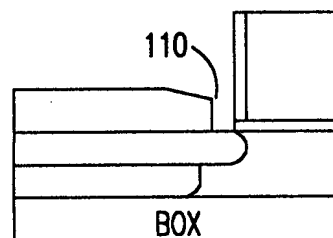


FIG. 11

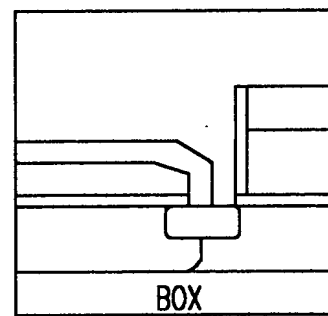
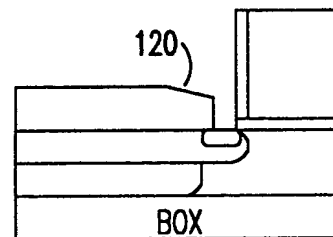
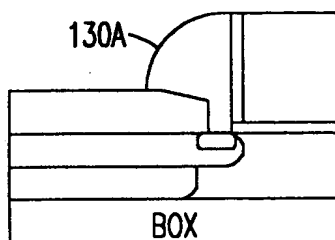


FIG. 12

FIG. 9

NFET



PFET

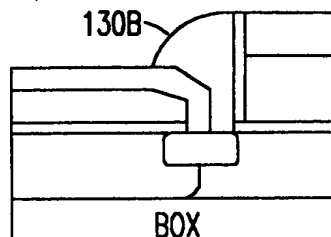


FIG. 13

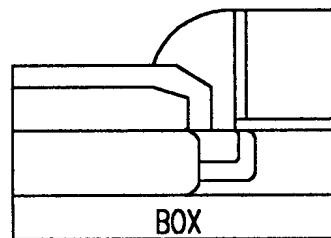
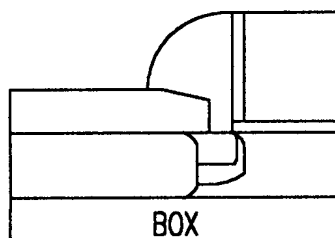


FIG. 14

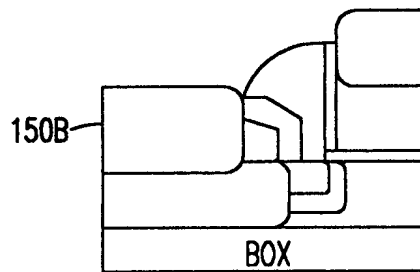
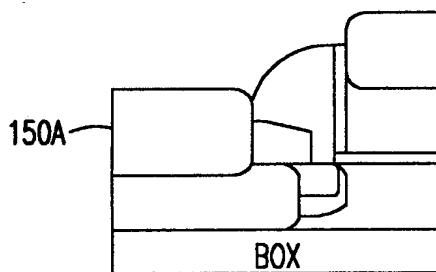


FIG. 15

FIG. 13